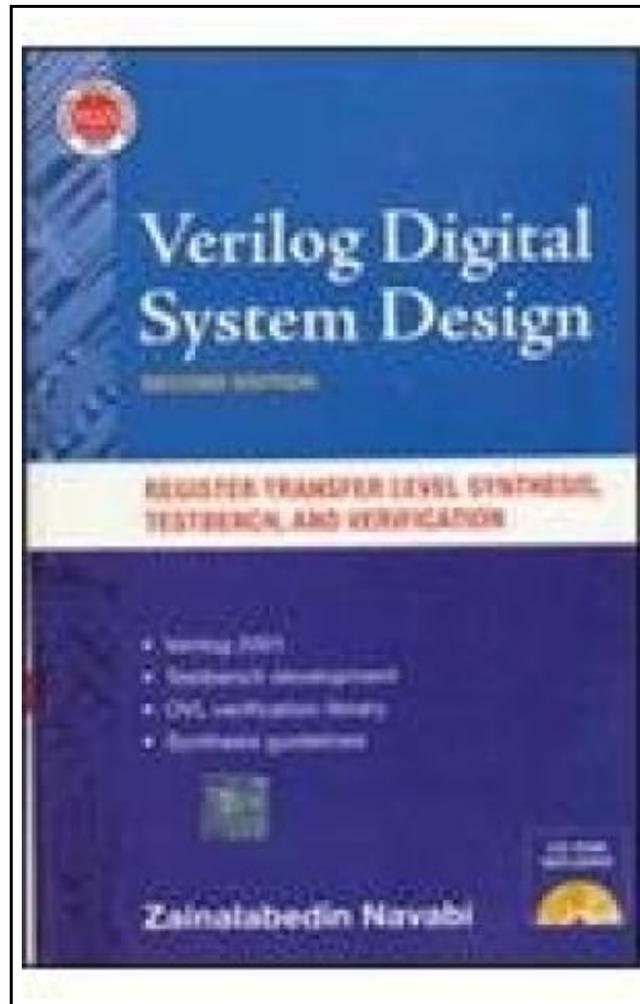# Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification, (Second Edition)

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Reviews

It is an awesome book that we have possibly go through. It is actually writter in straightforward words and phrases and not confusing. It is extremely difficult to leave it before concluding, once you begin to read the book.
(Tierra Kunde)

VERILOG DIGITAL SYSTEM DESIGN: REGISTER TRANSFER LEVEL SYNTHESIS, TESTBENCH, AND VERIFICATION, (SECOND EDITION)

Tata McGraw-Hill Education Pvt. Ltd., 2010. Softcover. Book Condition: New. 2nd edition. 15 x 23 cm. Verilog Digital System Design, 2/e, shows electronics designers and students how to apply verilog in sophisticated digital system design. Using over a hundred skill-building, fully worked-out, and simulated examples, this completely updated edition covers Verilog 2001, new synthesis standards, testing and testbench development, and the new OVL verification library. Moving from simple concepts to the more complex, Navabi interprets verilog constructs related to design stages and design abstractions, including behavioral, dataflow, and structure description. With emphasis on the concepts of HDLs. Clear specification and learning objectives at the beginning of each chapter and end-of-chapter problems focus attention on key points. Written by a HDL expert, the book provides: * Design automation with Verilog * Design with Verilog * Combinatorial circuits in Verilog * Sequential circuits in Verilog * Language utilities * Test methodologies * Verification * CPU design and verification MUST-HAVE CD INCLUDED * Verilog and VHDL simulators * Synthesis tools * Mixed-level logic and Verilog design environment * FPGA design tools and environments from Altera * Related tutorials and standards * All worked examples from the book, including testbench and simulationrun reports for every example * Complete CPU examples with Verilog code and software tools * OVL verification libraries and tutorials Printed Pages: 400.

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